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												1.0	WS Release	01/02/2021	SDD/KSS		
												1.1	ES Board (WS + MOD)	26/03/2021	SDD/KSS		
												2.0	MP Release	28/04/2021	SDD/KSS		
												2.1	Fitted CN1 header and Jumper	11/06/2021	SDD/KSS		
												2.2	Changed VCL capacitor value	17/06/2021	SDD/KSS		
												2.3	Changed main crystal frequency	05/07/2021	SDD/KSS		
<div></div> <h1>FPB-RA2E1</h1> <div>Table of Contents:</div> <div>Page 1 : Title Page Page 2 : Power Supply and HMI Features Page 3 : Arduino Shield Connection Page 4 : Pmod0 and Pmod1 Page 5 : MCU - RA2E1 Ports Page 6 : MCU - RA2E1 Clocks Page 7 : MCU - RA2E1 Power Page 8 : MCU - RA2E2 Page 9 : Pin Headers Page10: Debug Circuit</div>														Title			
														FPB-RA2E1			
														Title Page			
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TitleFPB-RA2E1

Title Page

SizeA3

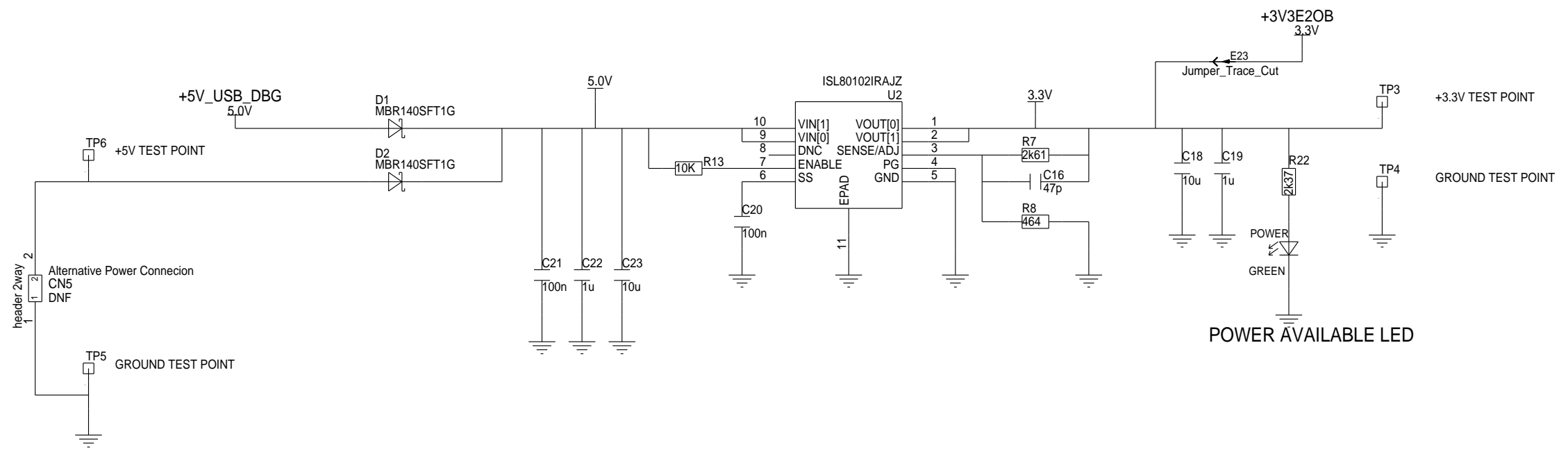
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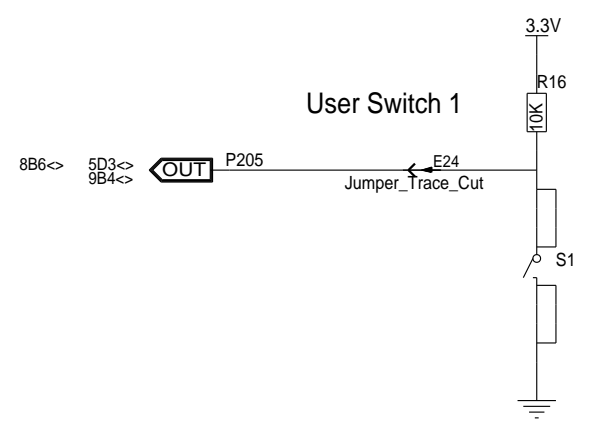
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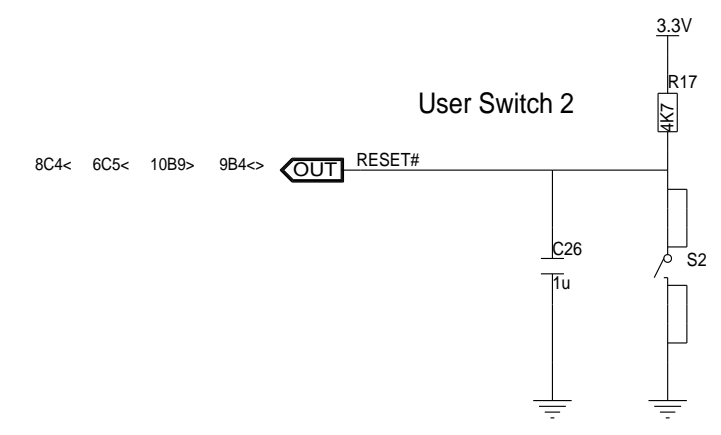
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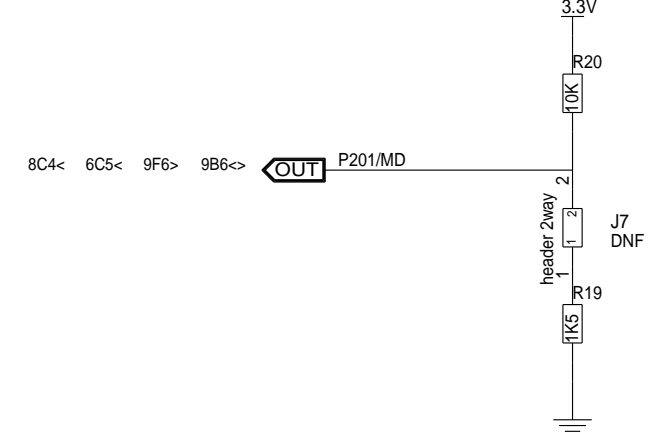
USER PUSH-BUTTONS



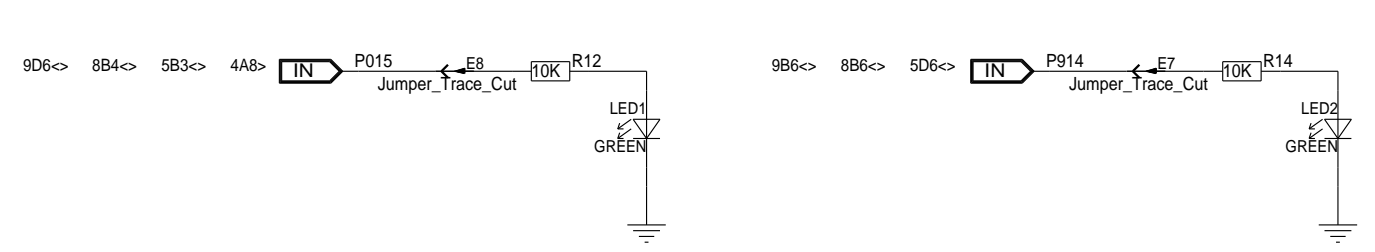
RESET PUSH-BUTTON



MCU BOOT MODE

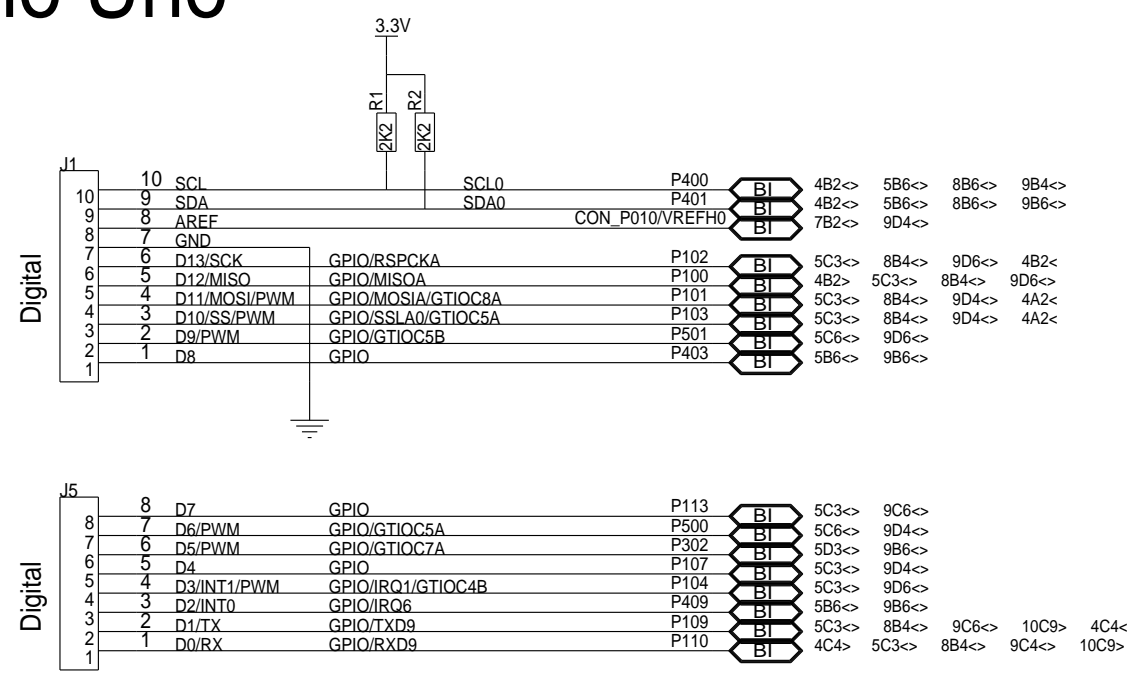
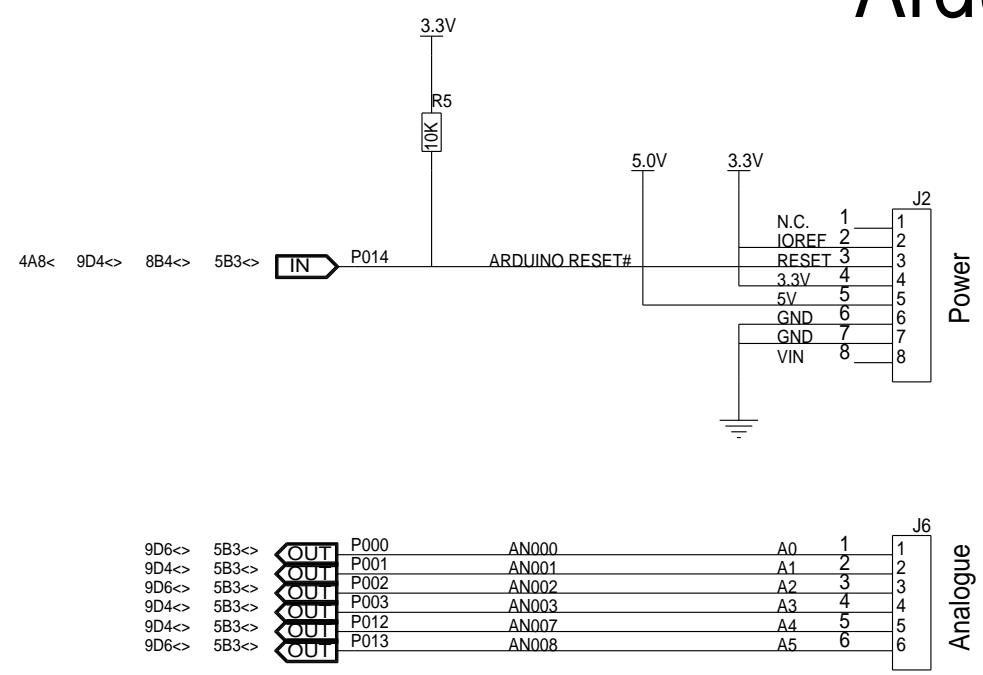


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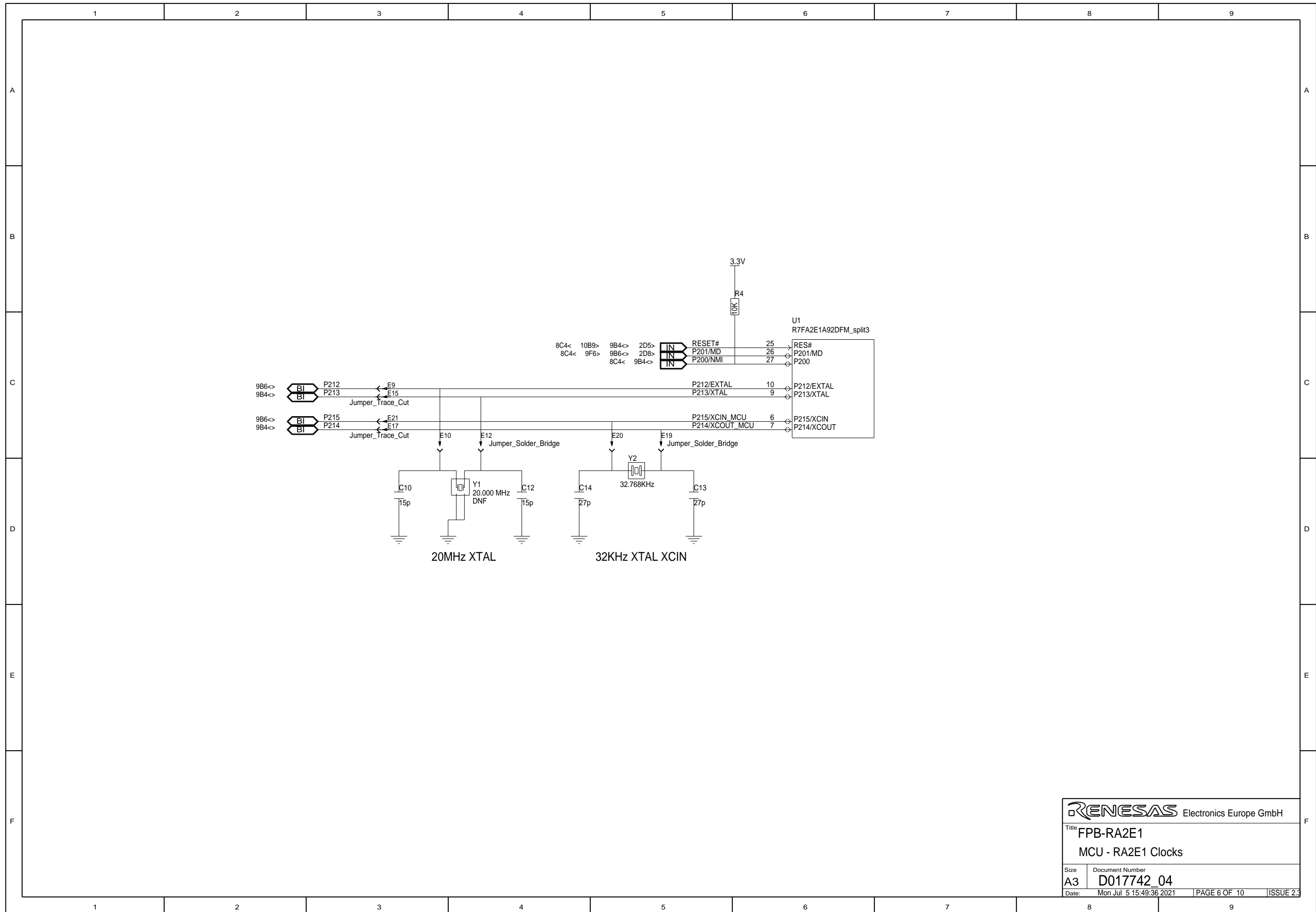


RENESAS Electronics Europe GmbH			
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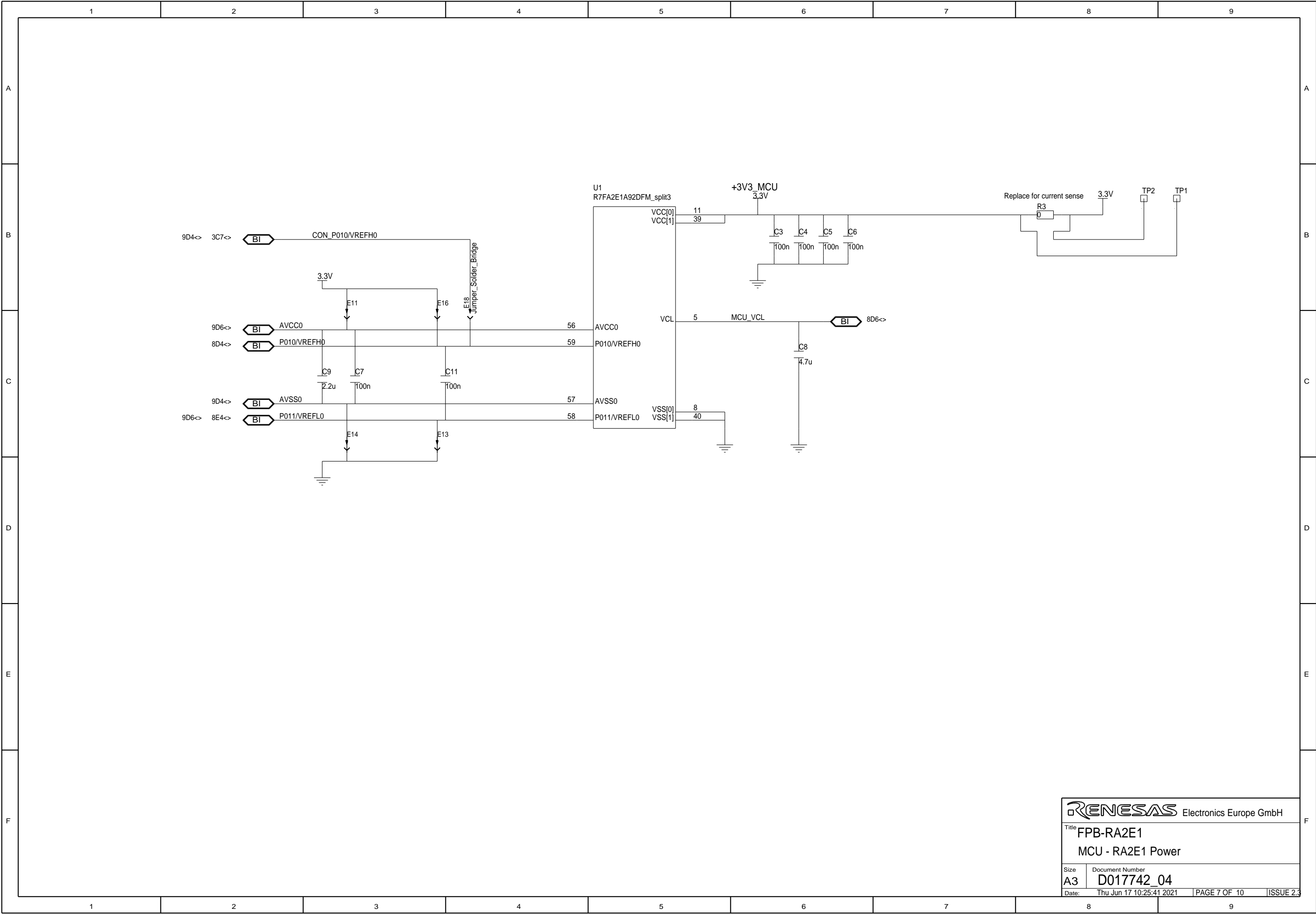
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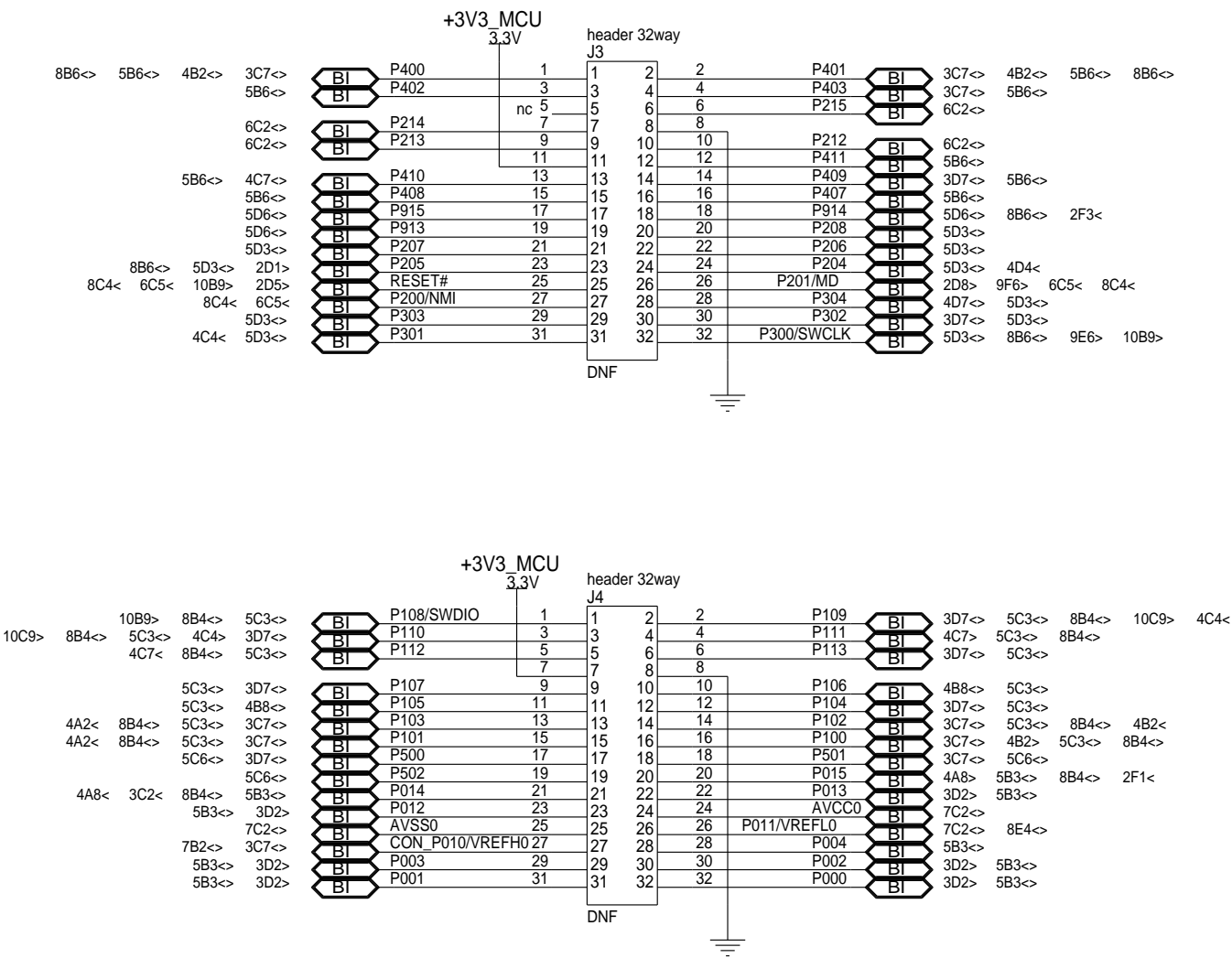


These intangible goods are not subject to Annex I of common Dual-Use list (428/2009) in its current version.

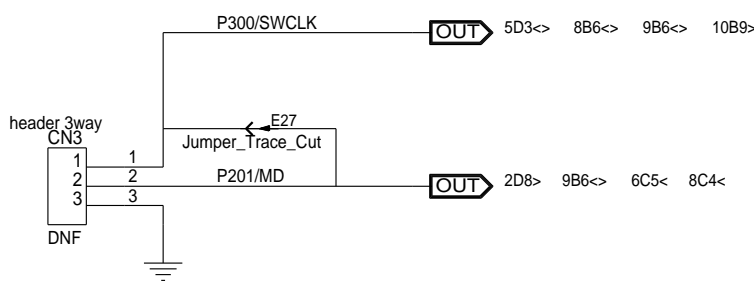




PIN HEADERS



DEBUG CONFIGURATION



Debug Circuit is company confidential

